Fully Industry Sponsored MTech Programme at IIT Delhi in VLSI Design, Tools and Technology

Programme Objectives
• Academia-industry collaboration
• Highly employable post-graduates
• Boost Indian semiconductor ecosystem

Specialization Streams
• ASIC and SoC Design
• Micro and Nano Devices
• Embedded Intelligent Systems

State-of-the-art Labs
• Multiple design and DSP labs
• Nano Research Facility (fab)
• CRF-AECL (Measurements)
  • 67 GHz 4 port VNA
  • 50 GHz +35dBm load-pull
  • Probe stations (DC/RF/Cryogenic)
  • Advanced low frequency noise analyser
  • Dilution refrigerator and multi-qubit characterization (planned)

Salient Features
• Two-year MTech programme, in existence since 1996, MS(Research) also possible with an extra sem.
• Attracts top GATE scorers in EC, CS, IN, PHY
• Sponsors choose candidates from a shortlist
• Core courses finished within first sem.
• Elective courses and their timing can be chosen by the sponsor
• One year project at sponsor’s site
• No upfront hiring commitment by the sponsor or the student, for July entry batch an MoU between IIT Delhi and sponsors needs to be in place a couple of months in advance
• MS(Research) can also start in January
• IIT Delhi Participation by EE, CSE and CARE
• Cost of sponsorship currently is Rs. 1.9 lac per student per semester. In case sponsor’s office is NOT located in an SEZ within India, there will be an additional 18% GST.

Sponsors

For more information:
Web: https://vdtt.iitd.ac.in, Email: adixit@ee.iitd.ac.in